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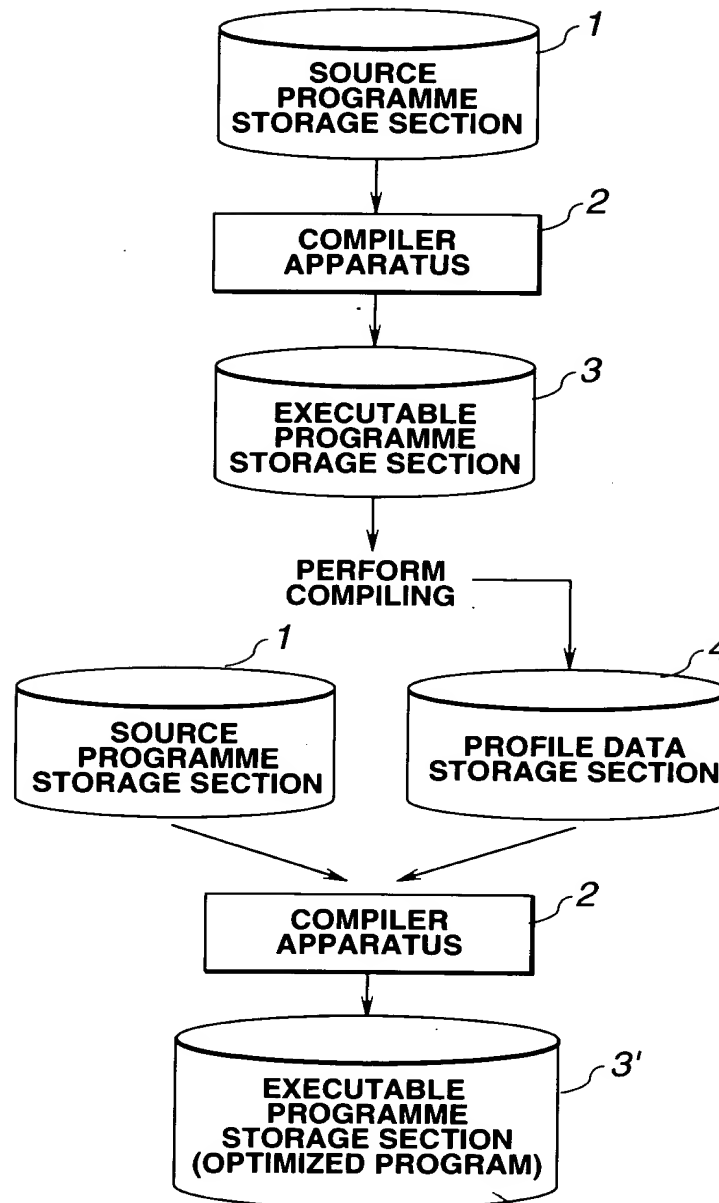


FIG.1

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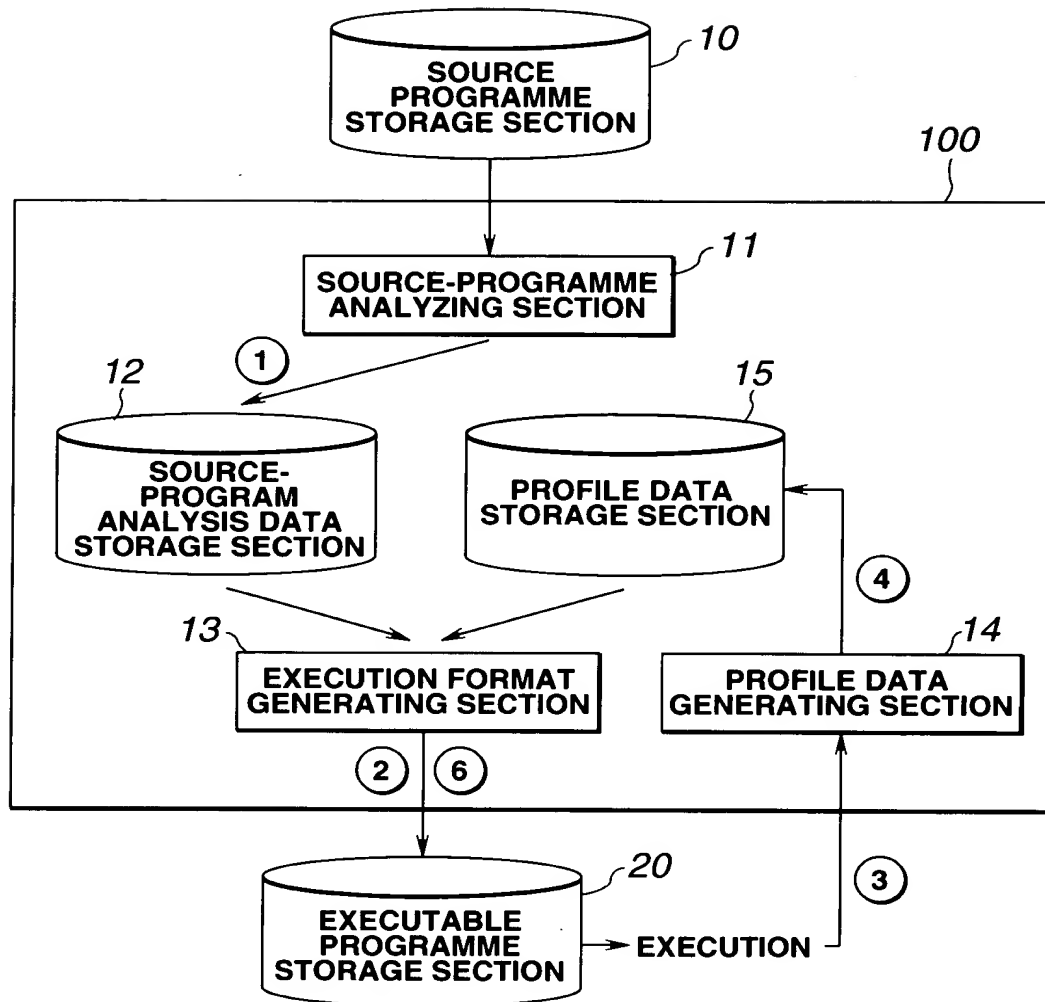


FIG.2



EXAMPLE OF A SOURCE PROGRAMME

```
if (a>b)      then c = a ;  
              else c = b ;  
  
d = c c ;
```

FIG.4

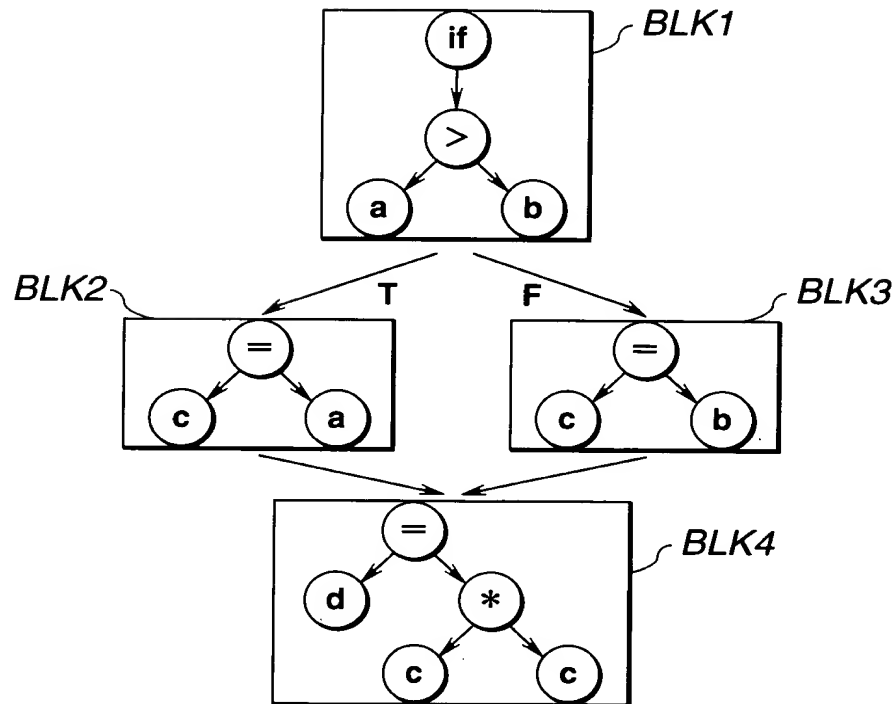


FIG.5

0005 RAM S 0 0790096009 M2

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ASSEMBLY CODE

```

.text
block_1 :
    ld    r1, [a]        ; r1<-a
    ld    r2, [b]        ; r2<-b
    cmp   r3, r1, r2     ; r3<-r1 cmp r2
    ble   r3, block_3
block_2 :
    ld    r4, [a]        ; r4<-a
    st    r4, [c]        ; c<-r4
    jmp   block_4
block_3 :
    ld    r5, [b]        ; r5<-b
    st    r5, [c]        ; c<-r5
    jmp   block_4
block_4 :
    ld    r6, [c]        ; r6<-c
    ld    r7, [c]        ; r7<-c
    mul   r8, r6, r7     ; r8<-r6 * r7
    st    r8, [d]        ; d<-r8

```

FIG.6

PROFILE DATA

TIME

DETAILED DATA

...		
10050:	PC	block_1
	load address	a
10051:	load address	b
10052:	compare	
10053:	branch less or equal	block_3
10054:	PC	block_3
	load address	b
10055:	store address	c
10056:	jump	block_4
10057:	PC	block_4
	load address	c
10058:	load address	c
10059:	mul	
10080:	store address	d
...		

FIG.7

LABEL NAME	EXECUTION/ NUMBER OF ACCESSES
...	
block_1 :	100
block_2 :	10
block_3 :	90
block_4 :	100
...	
a load:	110
a store:	0
b load:	190
b store:	0
c load:	200
c store:	100
d load:	0
d store:	100
...	

FIG.8

```

.text
block_1 :
    ld    r1, [a]           ; r1<-a
    ld    r2, [b]           ; r2<-b
    mv     r5, r2            ; r5<-r2
    cmp    r3, r1, r2        ; r3<-r1 cmp r2
    ble    r3, block_3

block_2 :
    ld    r4, [a]           ; r4<-a
    st     r4, [c]           ; c<-r4
    jmp     block_4

block_3 :
    st     r5, [c]           ; c<-r5
    mul    r8, r5, r5        ; r8<-r5 * r5
    st     r8, [d]           ; d<-r8
    jmp     block_5

block_4 :
    ld    r6, [c]           ; r6<-c
    ld    r7, [c]           ; r7<-c
    mul    r8, r6, r7        ; r8<-r6 * r7
    st     r8, [d]           ; d<-r8

```

FIG.9

[illegible]

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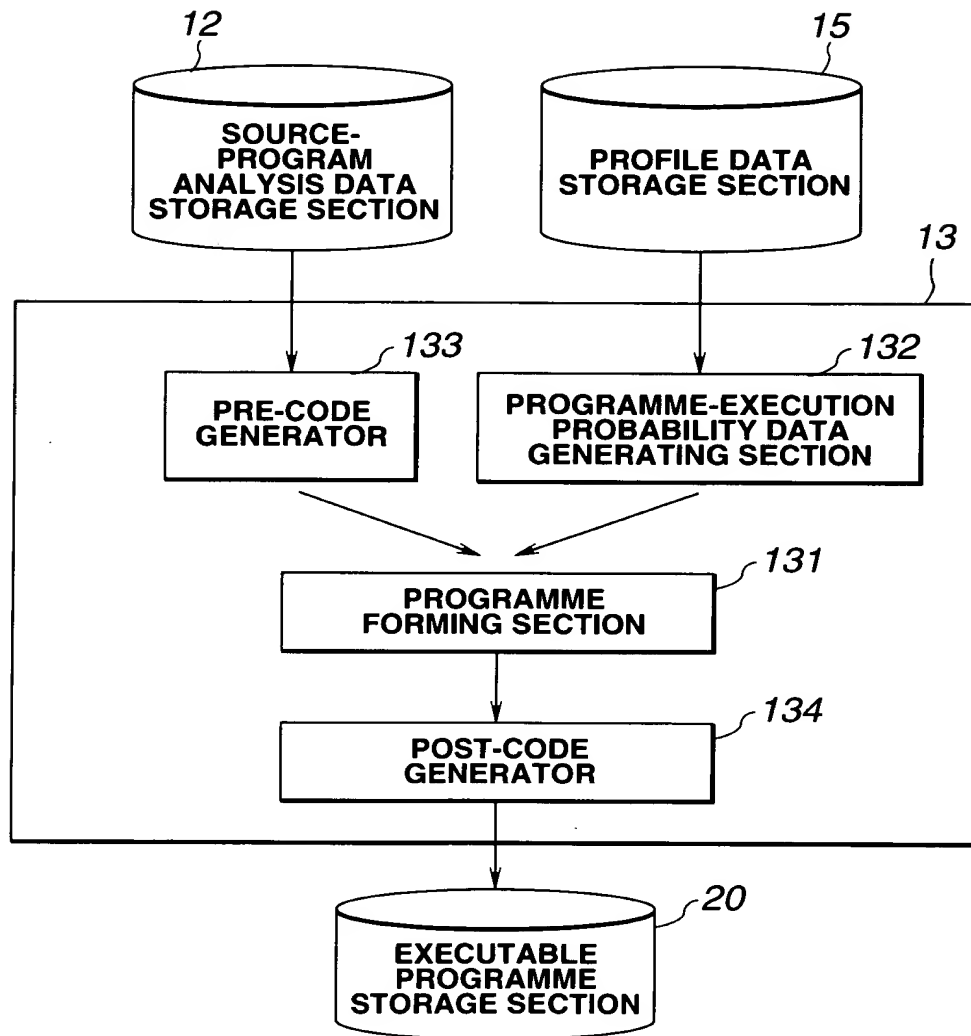


FIG.10



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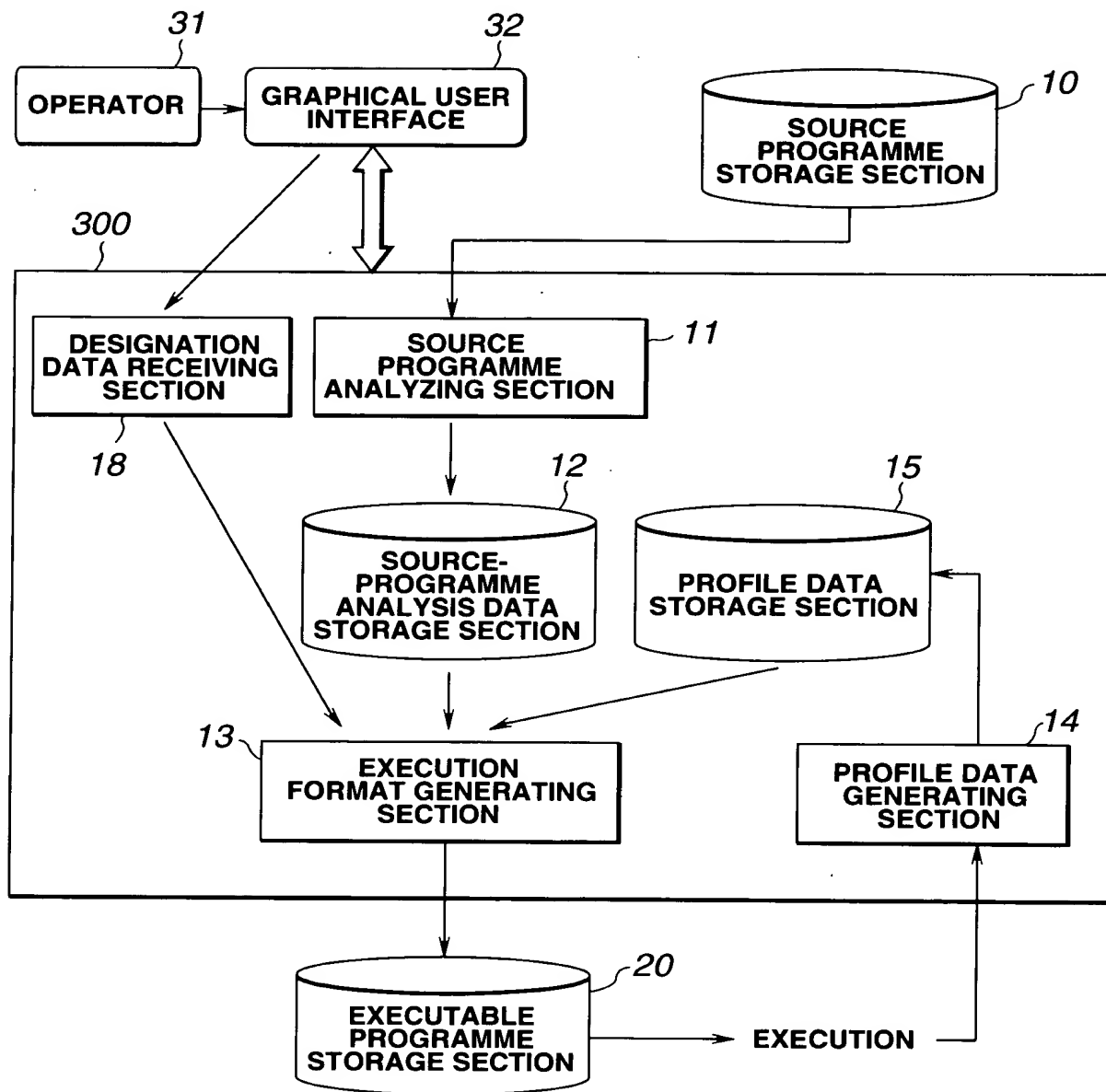


FIG.12

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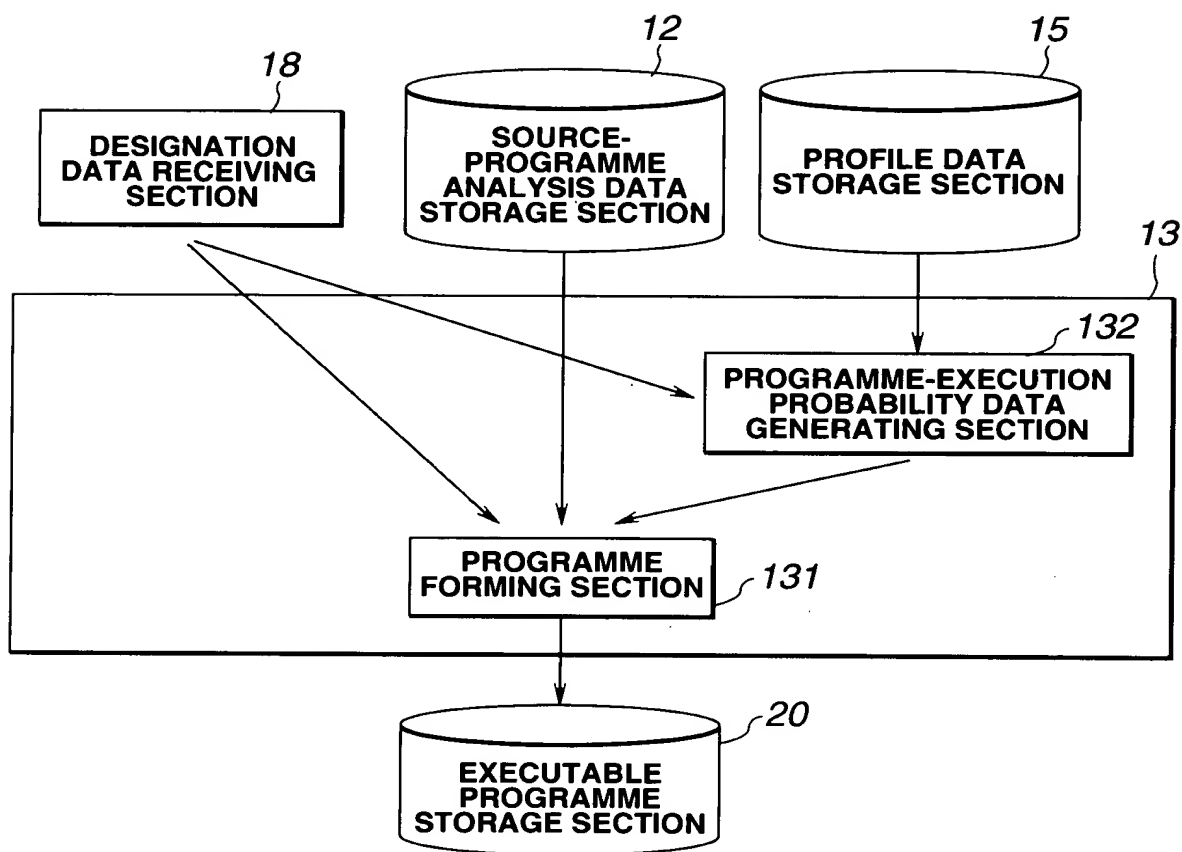


FIG.13

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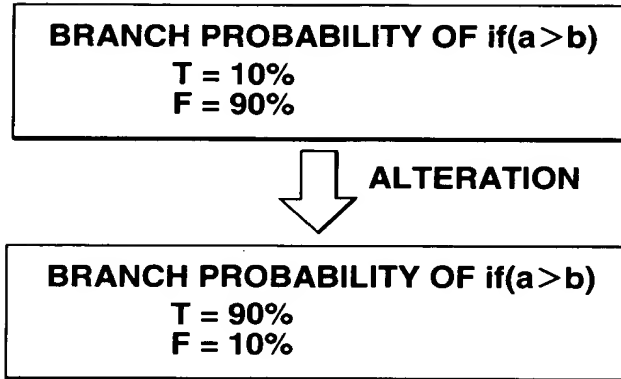


FIG.14

```

.text
block_1 :
    ld    r1, [a]        ; r1<-a
    ld    r2, [b]        ; r2<-b
    mv    r4, r1         ; r4<-r1
    cmp   r3, r1, r2     ; r3<-r1 cmp r2
    ble   r3, block_3
block_2 :
    ld    r4, [a]        ; c<-r4
    mul   r8, r4, r4      ; r8<-r4 * r4
    st    r8, [d]        ; d<-r8
    jmp   block_5
block_3 :
    st    r5, [b]        ; r5<-b
    st    r5, [c]        ; c<-r5
    jmp   block_4
block_4 :
    ld    r6, [c]        ; r6<-c
    ld    r7, [c]        ; r7<-c
    mul   r8, r6, r7      ; r8<-r6 * r7
    st    r8, [d]        ; d<-r8

```

FIG.15

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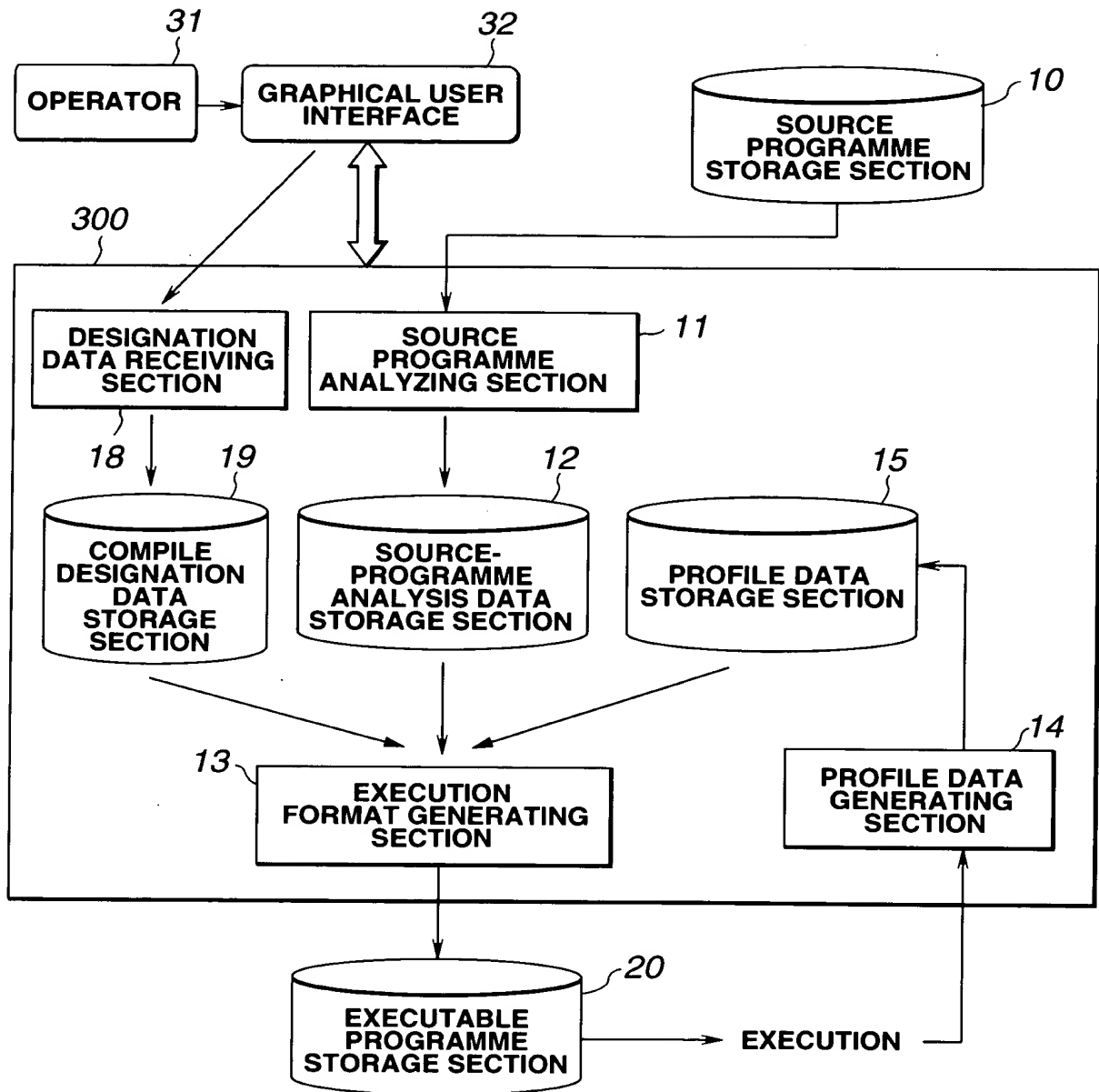


FIG.16